

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2450762
<b>Application Number:</b>	10602292
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	1233
<b>Title of Invention:</b>	PROCESSING SYSTEM WITH INTERSPERSED STALL PROPAGATING PROCESSORS AND COMMUNICATION ELEMENTS
<b>First Named Inventor/Applicant Name:</b>	Michael B. Doerr
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<b>Filer:</b>	Jeffrey C Hood/Jeannie Miranda
<b>Filer Authorized By:</b>	Jeffrey C Hood
<b>Attorney Docket Number:</b>	5860-00101
<b>Receipt Date:</b>	09-NOV-2007
<b>Filing Date:</b>	24-JUN-2003
<b>Time Stamp:</b>	17:12:08
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$810
RAM confirmation Number	2256

Deposit Account	501505
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

## File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	5860-00101_RCE.pdf	718056 308049472f106c5202dc2396c504b17a18960c51	no	3
<b>Warnings:</b>					
<b>Information:</b>					
2	Information Disclosure Statement (IDS) Filed	5860-00101_IDS_3.pdf	1011797 e75db4d57d64090228eaf00c3030ee864823d73	no	7
<b>Warnings:</b>					
<b>Information:</b>					
3	NPL Documents	Agarwal_Raw_Computation.pdf	1698686 422811c0db30ba11520a0e37a5d1e47fb4852a7	no	5
<b>Warnings:</b>					
<b>Information:</b>					
4	NPL Documents	Waingold_Baring_It_All_To_Software.pdf	1461053 074e4c8c0819d734498034c3c3d82bd4458a985	no	8
<b>Warnings:</b>					
<b>Information:</b>					
5	NPL Documents	Taylor_Raw_Microprocessor.pdf	1498203 918cc0864cf09c49a70ca475dd943dc283c057095	no	11
<b>Warnings:</b>					
<b>Information:</b>					
6	NPL Documents	Lee_Space-Time_Scheduling.pdf	1244559 59e18167a1379e70bc028418e9e5c554c142284	no	11
<b>Warnings:</b>					
<b>Information:</b>					
7	NPL Documents	Barua_Compiler_Support_for_Scalable_Efficient_Memory_Systems.pdf	1757406 85204c21b15a4b299739904213d39e322bcf914e	no	32
<b>Warnings:</b>					
<b>Information:</b>					
8	NPL Documents	Lee_Convergent_Scheduling.pdf	1212515 a7e932a89db13098d55a456ad03c792a6d63c9a55	no	12

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<b>Information:</b>					
9	NPL Documents	Babb_Parallelizing_Applications_into_Silicon.pdf	1086796 5c59ec9b71e91a24a4d182612884f26a3954f133	no	11
<b>Warnings:</b>					
<b>Information:</b>					
10	NPL Documents	Babb_RAW_Benchmark_Suite.pdf	840814 7ba93c358eb57314c2bc35741c1aac50fbb5d13d	no	10
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11	NPL Documents	Babb_Solving_Graph_Problems_with_Dynamic_Computation_Structures.pdf	621419 93cb699749266a3751472b173b173b1c3cd06106	no	12
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<b>Information:</b>					
12	NPL Documents	Frank_SUDS_Primitive_Mechanisms.pdf	923238 0ab2505bdc5d59968cac51eab98d2515304b6e1	no	9
<b>Warnings:</b>					
<b>Information:</b>					
13	NPL Documents	Barua_Maps_Compiler_Managed_Memory_System_for_Software_Exposed_Architectures.pdf	10312027 3ae21bae7cdaf7ba7cd0397de7413e1f8daa553	no	161
<b>Warnings:</b>					
<b>Information:</b>					
14	NPL Documents	Barua_Maps_Compiler_Managed_Memory_System_for_Raw_Machines.pdf	1204069 51cd2b4bf1c2b5e323306c1f6b3ebbb6e41075c8f	no	12
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<b>Information:</b>					
15	NPL Documents	Barua_Memory_Bank_Disambiguation.pdf	820796 a2c3aule929c857a2708109a1100f83bdce5e55	no	9
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<b>Information:</b>					
16	NPL Documents	Moritz_Hot_Pages_Software_Caching.pdf	1160072 d1ee75665d6eb42a5d8b32bf13252289b62f67b67	no	12
<b>Warnings:</b>					
<b>Information:</b>					
17	NPL Documents	Miller_Software_Based_Instruction_Caching.pdf	1349997 c9e7aabbec3aac29b666aac3e4391d16dbf8aa33	no	39

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<b>Information:</b>					
18	NPL Documents	Taylor_How_To_Build_Scal able_On-Chip_ILP_Networks. pdf	1067176 b6a038278c25e0d37d90a8b83c04b51 27017364	no	15
<b>Warnings:</b>					
<b>Information:</b>					
19	NPL Documents	Taylor_Scalar_Operand_Net works.pdf	1623119 f74e008b02ef3af72705021f82a1783be 00b313	no	20
<b>Warnings:</b>					
<b>Information:</b>					
20	NPL Documents	Taylor_Design_Decisions_I mplementation_Raw_Archite cture_Workstation.pdf	4574518 2627c561a699bcb5d91cd10b6d11e47 2efba0fe	no	90
<b>Warnings:</b>					
<b>Information:</b>					
21	NPL Documents	Moritz_Exploring_Optimal_C ost-Performance_Designs.pd f	1271217 d7b4993c45dd1a852a8b10a75424703 4db0f05	no	16
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<b>Information:</b>					
22	NPL Documents	Agarwal_Raw_Compiler_Pro ject.pdf	694023 402ac0f06be810d1170395a5eb01119a0 05d33b06	no	12
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<b>Information:</b>					
23	NPL Documents	Parker_Component_Based_ Architecture.pdf	986990 b310901da0f55710ad7bdc071b1a90d5 95aab393	no	14
<b>Warnings:</b>					
<b>Information:</b>					
24	NPL Documents	Agarwal_MIT_Alewife_Machi ne.pdf	1780730 9bcb84efca3d0c05307c76be4a78ac1ad 9759992	no	16
<b>Warnings:</b>					
<b>Information:</b>					
25	NPL Documents	Houzet_Shared_Memory_M odel_on_a_Cluster_of_PC. pdf	995832 3c1c062ba6c0b48993c21dc0511f0cd a0e09105	no	10
<b>Warnings:</b>					
<b>Information:</b>					
26	NPL Documents	Kimelman_Visualizing_Exec ution_of_High_Performance _Fortran.pdf	745608 b6110d3a672b7c778fad98363b3d216 3b9e9e7	no	10

**Warnings:****Information:**

27	Fee Worksheet (PTO-06)	fee-info.pdf	8223	no	2
			2b4627321794d32f8a2b2147b0e1a18e5 0000000		

**Warnings:****Information:**

Total Files Size (in bytes):	42670939
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This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

**New International Application Filed with the USPTO as a Receiving Office**

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.